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U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

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CLASS 348	SUBCLASS	ART UNIT 5 111	EXAMINER
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APPLICATION NO. 09/495427	CONT/PRIOR YES	DATE 348	SUBCLASS 2711	ART UNIT 2711 3612	EXAMINER <i>W. H. H. H.</i>
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Toshio Nakakuki

312

Villegga

Clock signal generator for solid-state imaging apparatus

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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Amount Due			Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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